Patent claims

1. A computer-aided method for parallel calculation of the operating point of electrical circuits,
   - in which the circuit is partitioned into a number of partitions in a first step,

   characterized in that
   - the charging method is used for the parallel calculation of the individual partitions.

2. The computer-aided method as claimed in claim 1, characterized in that a dynamic element (C, L) is provided and at least one node of the circuit.

3. The computer-aided method as claimed in claim 2, characterized in that a dynamic element (C, L) is provided at each node of the circuit.

4. The computer-aided method as claimed in claim 2 or 3, characterized in that each node of the circuit is connected by means of in each case one capacitance to in each case a predetermined value having in each case a potential so that an operating point of the modified circuit can be calculated.

5. The computer-aided method as claimed in claim 4, characterized in that a capacitance having the same value (C0) is provided at each node of a partition.

6. The computer-aided method as claimed in claim 4 or 5, characterized in that each node of a partition is connected to the same potential by means of a capacitance.

7. The computer-aided method as claimed in claim 4, characterized in that a capacitance having the same value (C0) is provided at each node of all partitions.

8. The computer-aided method as claimed in claim 4 or 7, characterized in that each node of all partitions is connected to the same potential by means of a capacitance.

9. The computer-aided method as claimed in one of claims 4 to 7, characterized in that the potential is connected to ground.

10. The computer-aided method as claimed in one of claims 4 to 8, characterized in that
- the operating point of the circuit is calculated in each case with a suitable step-by-step change in the value of (C) of the capacitance, and
- this step is repeated until the values of the capacitances are almost zero.

11. A computer program product which can be loaded into a main memory of a computer system, with a software code for carrying out the method according to one of the preceding claims when the computer program product is running on a computer system.

12. A data carrier with a computer program product as claimed in claim 11.

add 9'